CLAIMS:

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- 1. A method of operating a DC/DC up-down converter which has
  - an input voltage (U<sub>in</sub>) and at least a first and a second output voltage (U<sub>A</sub>, U<sub>B</sub>),
  - at least one inductive energy storage means (L<sub>1</sub>), which is connected with a first terminal (X<sub>1</sub>) to a main switching means (T<sub>1</sub>) and can be connected with a second terminal (Y<sub>1</sub>) to at least two outputs (A B) via switching means (T<sub>3</sub>, D<sub>3</sub>),
  - output switching means (T<sub>3</sub>, D<sub>3</sub>) for providing electrical energy for the first and second output voltages(U<sub>A</sub>, U<sub>B</sub>) by supplying a coil current (I<sub>L1</sub>),
  - a main switching means  $(T_1)$  between the inductive energy storage means  $(L_1)$  and a DC voltage source generating the input voltage  $(U_{in})$ ,
  - a free-wheeling switching means  $(T_2 D_2)$  which makes possible the continuation of the current flow in the inductive means  $(L_1)$  if the main switching means  $(T_1)$  is switched off and
  - a control means (controller) for selective actuation of all switching means (T<sub>1</sub>, T<sub>2</sub>, T<sub>3</sub>, T<sub>4</sub>),

wherein

- the first output voltage (U<sub>A</sub>), which is lower than the input voltage (U<sub>in</sub>), is present on the first output (A) and
- the second output voltage (U<sub>B</sub>), which is higher than the input voltage (U<sub>in</sub>), is present on the second output (B)
  - at least a further switching means (T<sub>3</sub>) for controlling the direction of the coil current (I<sub>L1</sub>) into the first output (A) or into the second output (B) is connected in series with the first output (A),

characterized in that the control means (controller)

- -- controls the output switching means  $(T_3, T_4)$ , so that in the course of one switching cycle  $(SZ_1, SZ_2)$  the coil current  $(I_{L1})$  flows from the second terminal  $(Y_1)$  into both output branches (A, B) and
- -- controls the main switch (T<sub>1</sub>) in the transient state of the up-down converter, so that the average voltage on the first terminal (X<sub>1</sub>) is equal to the voltage on the second terminal (Y<sub>1</sub>).
- A method as claimed in claim 1 in which the control means (controller) generates switching phases (Φ<sub>2</sub>, Φ<sub>3</sub> and Φ<sub>5</sub>, Φ<sub>6</sub>, respectively) for the switching means
   (T<sub>1</sub>, T<sub>2</sub>, T<sub>3</sub>, T<sub>4</sub>) and the course of the coil current (I<sub>L1</sub>) comprises an up-conversion phase and a down-conversion phase, characterized in that the down-conversion phase of the coil current (I<sub>L1</sub>) comprises at least two switching phases (Φ<sub>2</sub>, Φ<sub>3</sub> and Φ<sub>5</sub>, Φ<sub>6</sub>, respectively).
- 15 3. A method as claimed in claim 2, characterized in that the switching cycle (SZ1, SZ2) has all the switching phases ( $\Phi_1$ ,  $\Phi_2$ ,  $\Phi_3$  and  $\Phi_4$ ,  $\Phi_5$ ,  $\Phi_6$ , respectively), exactly once.
- 4. A method of operating a DC/DC up-down converter which has
  20 an input voltage (U<sub>in</sub>) and at least a first and a second output voltage (U<sub>D</sub>, U<sub>E</sub>),
  - at least one inductive energy storage means (L<sub>2</sub>), which is connected with a first terminal (X<sub>2</sub>) to a DC voltage source generating in the input voltage (U<sub>in</sub>) and can be connected with a second terminal (Y<sub>2</sub>) to the outputs (D, E) via the switching means (T<sub>6</sub>, D<sub>4</sub>),
- output switching means T<sub>6</sub>, D<sub>4</sub>) for providing electrical energy for the first and the second output voltage (U<sub>D</sub>, U<sub>E</sub>) by supplying a coil current (I<sub>L2</sub>),
  - a main switching means (T<sub>5</sub>) between a second terminal (Y<sub>2</sub>) of the inductive energy storage means (L<sub>2</sub>) and the other pole of the DC voltage source, and
- a control means (controller) for selectively actuating all switching means ( $T_5$ ,  $T_6$ ,  $T_7$ ),

wherein

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- the first output voltage (U<sub>D</sub>), which is lower than the input voltage (U<sub>in</sub>), is present on the first output (D) and
- the second output voltage (U<sub>E</sub>), which exceeds the input voltage (U<sub>in</sub>), is present on the second output (E),
- at least a further switching means (T<sub>6</sub>) for controlling the direction of the coil current (I<sub>L2</sub>) into the first output (D) or into the second output (E) is connected in series with the first output (D),

characterized in that the control means (controller)

- -- controls the output switching means (T<sub>6</sub>, T<sub>7</sub>), so that in the course of one switching cycle (SZ<sub>3</sub>, SZ<sub>4</sub>) the coil current (I<sub>L2</sub>) flows from the second terminal (Y<sub>2</sub>) into both output branches (D, E, F) at least once and and
  - -- controls the main switch  $(T_5)$  in the transient state of the up-down converter so that the average voltage on the second terminal  $(Y_2)$  of the cloil  $(L_2)$  is equal to the voltage on the first terminal  $(X_1)$ , thus equal to the input voltage  $(U_{in})$ .
- A method as claimed in claim 4, wherein the control means (controller) generates switching phases (Φ<sub>7</sub>, Φ<sub>8</sub>, Φ<sub>9</sub> and Φ<sub>10</sub>, Φ<sub>11</sub>, Φ<sub>12</sub>, Φ<sub>13</sub> respectively) for each switching means (T<sub>5</sub>, T<sub>6</sub>, T<sub>7</sub>) and the pattern of the coil current (I<sub>L2</sub>) has an upconversion phase and a down-conversion phase, characterized in that the upconversion phase of the coil current (I<sub>L2</sub>) comprises at least two switching phases (Φ<sub>7</sub>, Φ<sub>8</sub> and Φ<sub>10</sub>, Φ<sub>11</sub> respectively).
- 6. A method as claimed in claim 5, characterized in that the switching cycle
   25 (SZ<sub>3</sub>, SZ<sub>4</sub>) comprises all switching phases (Φ<sub>7</sub>, Φ<sub>8</sub>, Φ<sub>9</sub> and Φ<sub>10</sub>, Φ<sub>11</sub>, Φ<sub>12</sub>, Φ<sub>13</sub>, respectively), exactly once.
- 7. A method as claimed on one of the preceding claims, characterized in that the switching means (T<sub>1</sub>, T<sub>2</sub>, ..., T<sub>7</sub>) are MOSFETs; IGBTs, GTOs or bipolar
   30 transistors.

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8. Implementation of a method as defined in the Claims 1 to 9, for the operation of a DC/DC up-down converter in electronic appliances in which consumers are to be supplied with different voltages such as, for example, in mobile telephones, PDAs (Personal Digital Assistants) or MP3 players.